



# PI-MAX®4: 1024 EMB



The PI-MAX4: 1024 EMB from Princeton Instruments is the ultimate in ICCD technology. This innovative intensified EMCCD camera (emICCD) features back-illuminated 1024 x 1024 frame transfer EMCCD fiberoptically coupled to a variety of Gen II and Gen III filmless intensifiers. The advantages of intensifiers and the benefits of EMCCD packed in one camera delivers single-photon sensitivity and quantitative performance for scientific imaging and spectroscopy research. The higest linearity, high sensitivity, ultrafast subnanosecond gating, dual gain control, programmable timing generator as well as other built in features makes these emICCD cameras ideal for the most demanding research!

FEATURES	BENEFITS			
Intensified EMCCD (emICCD) with Dual Gain mechanism	Dual Gain Mechanism allows single photon sensitivity and improves linearity			
1024 x 1024 FT (frame transfer)Imaging Array	Allows higher frame rates with 100% duty cycle			
10 MHz / 16-bit digitization	Video frame rates and higher to efficiently synchronize with high repetition rate lasers			
Thermoelectric cooling	Delivers low dark current and maintenance-free operation			
Kinetics Mode	Allows high speed burst mode sub-frame (ROI) imaging and spectroscopy based on the window size			
A selection of Intensifiers  Gen II  Gen III filmless	Best sensitivity and gate speed in the desired wavelength range; Best combination of UV-Blue sensitivity and fast gating (SB); RB provides wide spectral coverage Offers highest sensitivity and fastest gate speed			
Fiberoptic coupling	Highest optical throughput; No vignetting			
Sub-nanosecond gating	Provides <500 ps gate width with standard fast gate intensifiers while preserving QE for high temporal resolution; For effective background discrimination, kinetics imaging and spectroscopy			
Super HV - Built-in high voltage pulser	Rugged design for high rep rate gating and minimal insertion delay			
SuperSYNCHRO - Built-in programmable timing generator	Built-in, fully software controlled gate timing; Controls gate widths and delays in linear, or exponential increments; Low insertion delay (~27 ns). See page 3 for more info.			
GigE interface	Industry standard for fast data transfer over long distances			
<b>Optional:</b> LightField® (for Windows 8/7, 64-bit) Or WinView/Spec (for Windows 8/7/XP, 32-bit)	Flexible software packages for data acquisition, display and analysis; LightField offers intuitive, cutting edge user interface, IntelliCal® and more.			
PICAM (64-bit) / PVCAM (32-bit) software development kits (SDKs)	Compatible with Windows 8/7/XP, and Linux; Universal programming interfaces for easy custom programming.			
LabVIEW Scientific Imaging Tool Kit (SITK®)	Pre-defined LabView vis provide easy integration of the camera into complex experiment setup			

#### **Applications:**

Fluorescence Lifetime Imaging Microscopy (FLIM) | Time Resolved Imaging & Spectroscopy | Combustion | Photon Counting Planar Laser Induced Fluorescence (PLIF)

PI-MAX4:1024EMB Rev. P1.1 Page 1 of 8





CCD	PI-MAX4:1024 EMB						
CCD Image Sensor	e2v CCD 201; scientific grade; back-illuminated, frame transfer CCD						
CCD Format	1024 x 1024 imaging pixels; 13.0 x 13.0 μm pixels Effective image size: 1024 x 1024 pixels, 13.0 x 13.0 (17.5 mm diagonal)						
	EM mode Nor			Normal CCD mo	rmal CCD mode		
System read noise (typical)	< 55 e- @ 5 MHz < 90 e- @ 10 MHz Read noise effectively reduced to <1 e- rms with on-chip multiplication gain used			< 9 e- @ 500 KHz < 15 e- @ 1 MHz < 20 e- @ 5 MHz			
Full well (typical)		400 ke- (d	output node)		> 80 ke- (single pixel)		
Dark current @ -20° C (typical)	<= 2.5 e-/	p/sec					
Deepest cooling temperature @ 20° C ambient	-20° C (Air) -30° C (Water assist)						
Vertical Shift Rate*	3.4 µs/row	3.4 µs/row					
INTENSIFIER							
Intensifiers available	18 mm - Gen II, Gen III filmless 1:1 fiber optic						
Method of coupling to the CCD							
Intensifier type	Gen II		Gen III Filmless				
	SB	RB	SR	UV	HRf	HBf	InGaAs
Wavelength Range	See QE cur	ves					
Min. Gate Width (Optical FWHM) ** Sub-nanosecond Gate Fast Gate	< 500 ps (for Fast Gate tubes only) ~ 2.5 nsec (Typ), 3 nsec (Guar)			< 500 ps (for Fast Gate tubes only) ~ 2 nsec (Typ), 3 nsec (Guar)			
Repetition Rate: Sustained	1 MHz; 100 kHz with Picosecond gating; 8 kHz with MCP gating; 6.25 kHz with MCP bracket pul					oracket pulsing	
Resolution limit	40 to 64 lp/mm			57 to 64 lp/mm			
EBI Photo e-/pixel/sec @ room temp (with photocathode cooling)	0.05 - 0.2 (0.005 - 0.02)			0.02 (0.002)			
Phosphor	P43 (P46 and P47 optional)						
Operating Environment	+5° C to +	30° C non-	condensing				
Storage Environment	-25° C to +55° C						
Certification	CE						

All specifications subject to change. Contact your local sales representative for more information.

PI-MAX4:1024EMB Rev. P1.1 Page 2 of 8

<sup>\*</sup> Please refer to user's manual for more accurate timing calculations.

<sup>\*\*</sup> Measured with 18 mm intensifier



#### SuperSYNCHRO Timing Generator

The PI-MAX4's integrated SuperSYNCHRO Timing Generator lets researchers set gate pulse widths and delays under GUI software control. The closed coupled SuperSYNCHRO significantly reduces the system delay inherent in the timing generator of emICCD cameras. The integrated timing generator means there is no need for an additional external timing generator, and a built-in Super HV high voltage pulser eliminates the requirement for an external high-voltage supply, making the PI-MAX4 camera one of the most advanced ICCD cameras on the market.

FEATURE	BENEFITS
Closed Coupled Design	Short signal paths for minimum insertion delays
On-board memory	Store and execute complex gate width/delay sequences with no software overhead
Internal oscillator *	Drive an external event and initiate repetitive experiments.
SyncMASTER Pulses	Independent continuous TTL outputs to trigger pulsed external devices, e.g. laser and Q-switch; Minimum experiment jitter
Configurable Trigger inputs	Synchronizes camera to a wide variety of standard and non-standard trigger sources.
Full Software Control	Easy setup and execution of complex gate width/delay sequences

### SuperSYNCHRO Specifications

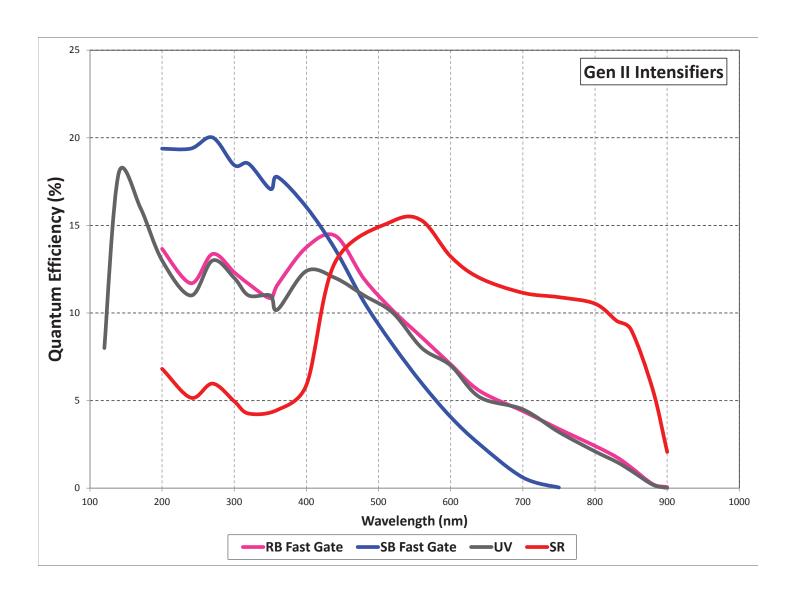
Internal Timing Generator	0.05 Hz - 1 MHz
Gate Delay + Width Range*	$\sim$ 10 ns ( $\sim$ 20 ns with MCP bracket pulsing) to 21 sec (from T0)
Timing resolution/ Timing jitter	10 ps / 35 ps rms
Insertion delay	$\sim$ 27 ns (trigger in to intensifier opening), $\sim$ 35 ns (with picosecond gating option)
TRIGGER INPUTS	
External Sync (Trigger In)	-5 v to $\pm$ 5 v (including TTL); AC/DC coupling: 50 ohm / High Z Variable Threshold; $\pm$ ve or -ve edge
TRIGGER OUTPUTS	
SyncMASTER <sub>1</sub>	Programmable continuous frequency output to synchronize external devices with PI-MAX4, e.g. Laser
SyncMASTER <sub>2</sub>	Programmable continuous frequency output (delay from SyncMASTER <sub>1</sub> - 100 ns - 6.55 msec) synchronize external devices with PI-MAX4, e.g. Q-switch
ТО	TTL Signal: T0 indicates start of timing sequence
Monitor**	TTL signal to monitor gate timing
Ready	TTL signal. Represents camera status. It changes state when ready just before the exposure.
Aux	DC coupled programmable delay (Delay from $T0 > 2ns - 1$ sec) trigger output to synchronize external devices with PI-MAX4
Logic	Software programmable: Select one of the following signals: Acquiring, Image Shift, Logic 1, Readout, Shutter or Wait for trigger. See users' manual for detailed signal descriptions.

<sup>\*</sup> Software programmable

PI-MAX4:1024EMB Rev. P1.1 Page 3 of 8

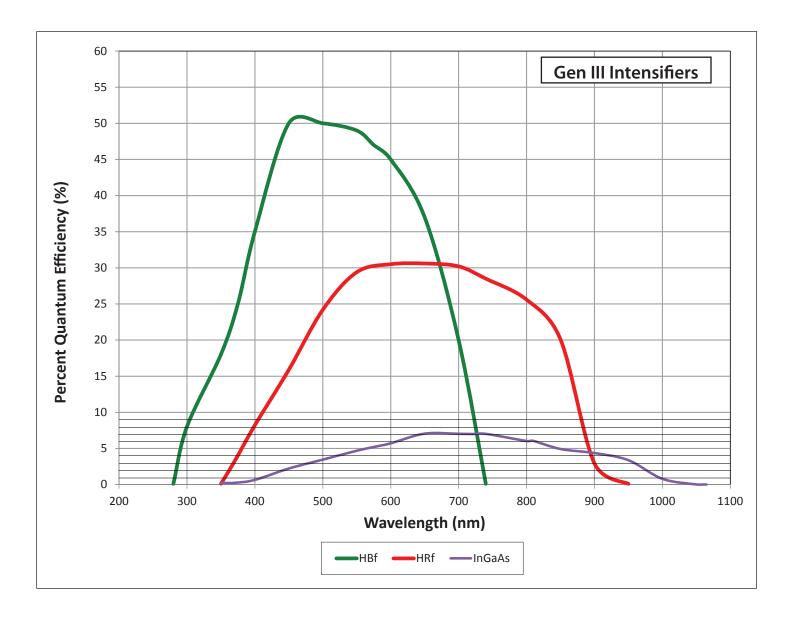
<sup>\*\*</sup> Please refer to user's manual for more accurate timing calculations.





PI-MAX4:1024EMB Rev. P1.1 Page 4 of 8





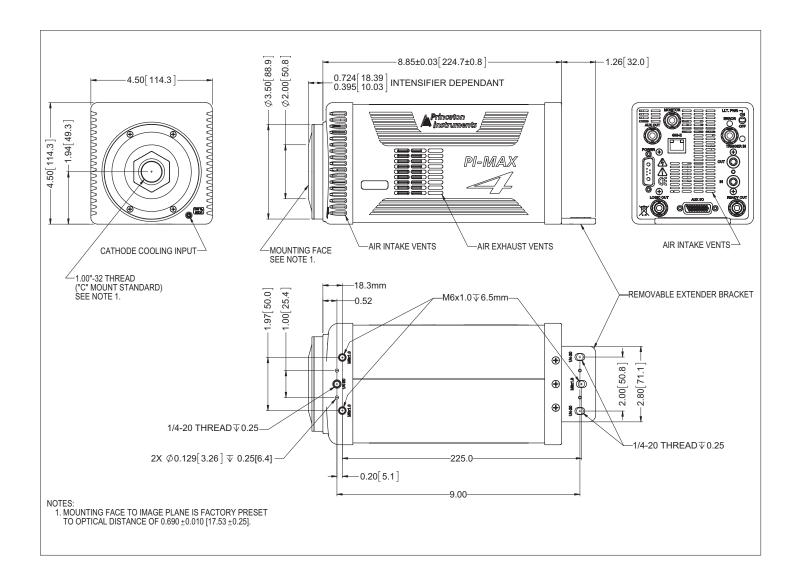
## Frame Rate (fps)

)	ROI/Bin	1024 x 1024	512 x 512	256 x 256	128 x 128	64 x 64	32 x 32
	1 x 1	8.69	16.94	32.36	59.17	102	1 <i>57.7</i> 3
	2 x 2	16.94	32.36	<i>5</i> 9.1 <i>7</i>	102	1 <i>57.7</i> 3	218.34
	4 x 4	32.36	59.1 <i>7</i>	102	1 <i>57.7</i> 3	218.34	270.27
	8 x 8	59.17	102	1 <i>57.</i> 73	218.34	270.27	306.74

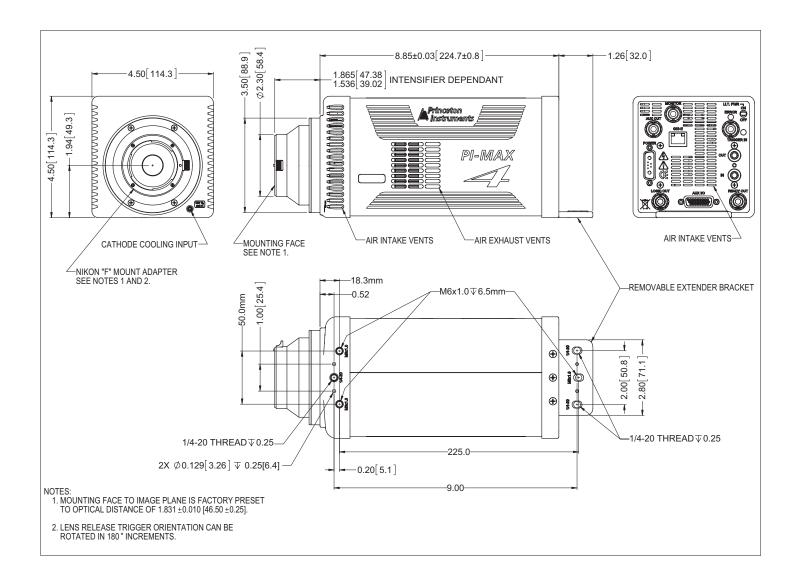
NOTE: Frame rate measured at 10 MHz digitization.

PI-MAX4:1024EMB Rev. P1.1 Page 5 of 8



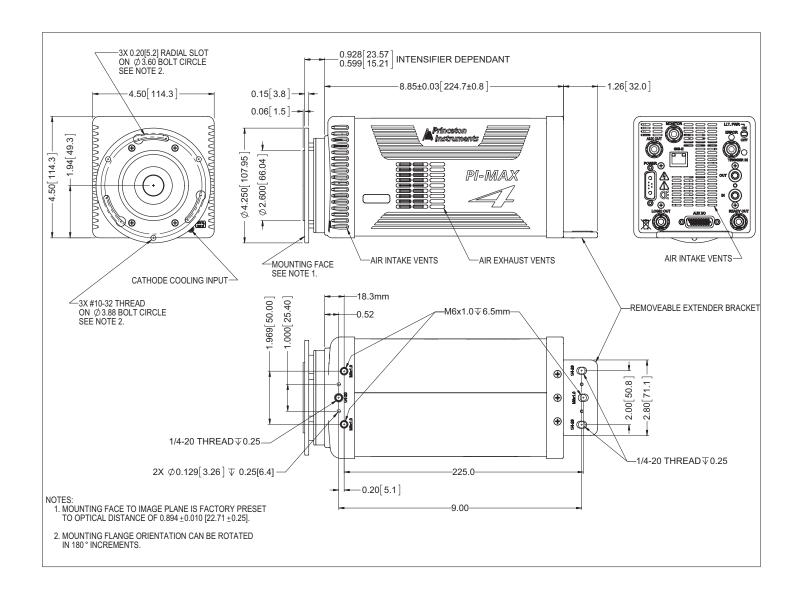






PI-MAX4:1024EMB Rev. P1.1 Page 7 of 8





PI-MAX4:1024EMB Rev. P1.1 Page 8 of 8